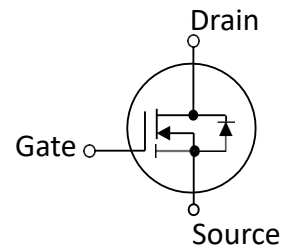


**150V N-Channel MOSFET**
**SYMBOL**


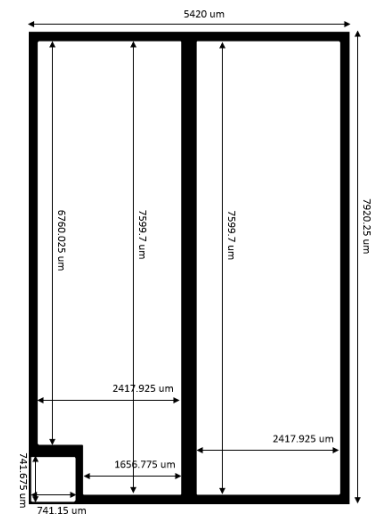
- Advanced Split Gate Device Design and Processes
- High Reliability Capability
- 100% CP Probing

**Electrical Characteristics in C/P Test ( $T_J$  at 25 °C)**

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Condition
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	150	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
$R_{DS(ON)}$	Static Drain-Source On-Resistance	—	2.0	2.4	m $\Omega$	$V_{GS} = 10V, I_D = 5A^{(1)}$
$V_{GS(th)}$	Gate Threshold Voltage	3	—	4.6	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
$I_{DSS}$	Drain-to-Source Leakage Current	—	—	1	$\mu A$	$V_{DS} = 150V, V_{GS} = 0V$
$I_{GSS}$	Gate-to-Source Leakage Current	-100	—	100	nA	$V_{DS} = 0V, V_{GS} = \pm 20V$
$T_J, T_{STG}$	Operating and Storage Temperature	-55°C to 150°C Max.				

**Mechanical Data**
**Die Drawing**

Chip Size <sup>(2)</sup>	7920 $\mu m$ X 5420 $\mu m$
Gate Pad Size	741 $\mu m$ X 741 $\mu m$
Source Pad Size	7600 $\mu m$ X 2418 $\mu m$
Scribe Line Width	60 $\mu m$
Wafer Thickness	100 $\mu m$
Wafer Diameter	200 mm
Gross Die	609 EA
Source Metallization	Al-Cu (4 $\mu m$ typical)
Drain Metallization	Ti-Ni-Ag
Passivation	Yes
Recommended Storage Environment	Store in original container, in dry nitrogen, 6 months at ambient temperature of 23°C $\pm$ 3°C



(1) Pulse Width  $t_p = < 1$  mS, Duty Cycle  $< 2\%$ .

(2) Chip size not include scribe line.



**Electrical Characteristics in F/T Test ( $T_J$  at 25 °C)**

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Condition
$I_{DSS}$	Drain-to-Source Leakage Current	—	—	1	$\mu A$	$V_{DS} = 150V, V_{GS} = 0V$
$I_{GSSF}$	Gate-to-Source Leakage Current	—	—	100	nA	$V_{DS} = 0V, V_{GS} = +20V$
$I_{GSSR}$	Gate-to-Source Leakage Current	-100	—	—	nA	$V_{DS} = 0V, V_{GS} = -20V$
$BV_{DSS}$	Drain-Source Breakdown Voltage	150	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
$BV_{DSS}$	Drain-Source Breakdown Voltage	150	—	—	V	$V_{GS} = 0V, I_D = 1mA$
$R_{DS(ON)}$	Static Drain-Source On-Resistance	—	—	2.9	m $\Omega$	$V_{GS} = 10V, I_D = 100A$
$V_{GS(th)}$	Gate Threshold Voltage	3	—	4.6	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
$V_{SD}$	Drain-Source Diode Forward Voltage			1.2	V	$V_{GS} = 0V, I_{SD} = 10A$
EAS test	IAS				A	$V_{DD} = 80V, V_{GS} = 10V, R_G = 25\Omega, L = 0.1mH$
$T_J, T_{STG}$	Operating and Storage Temperature	-55°C to 150°C Max.				

**Disclaimer:**

JUNSHINE does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

JUNSHINE reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

JUNSHINE makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, JUNSHINE disclaims (1) any and all liability arising out of the application or use of any product, (2) any and all liability, including without limitation special, consequential or incidental damages, and (3) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

JUNSHINE products, except as expressly indicated in writing, are not designed for use in medical, life-saving, or life-sustaining applications, or for any other application in which the failure of the JUNSHINE product could result in personal injury or death. Customers using or selling JUNSHINE products not expressly indicated for use in such applications do so at their own risks.

Resale of JUNSHINE products with statements different from or beyond the parameters stated by JUNSHINE for that product or service voids all express or implied warranties for the associated JUNSHINE product or service and is unfair and deceptive business practice. JUNSHINE is not responsible or liable for any such statements.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of JUNSHINE. Product names and markings noted herein may be trademarks of their respective owners.

JUNSHINE IS A FULLY OWNED SUBSIDIARY OF Wuxi XICHANWEIXIN Semiconductor Co., Ltd.