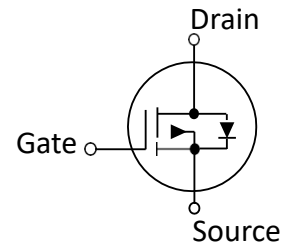


40V P-Channel MOSFET
SYMBOL

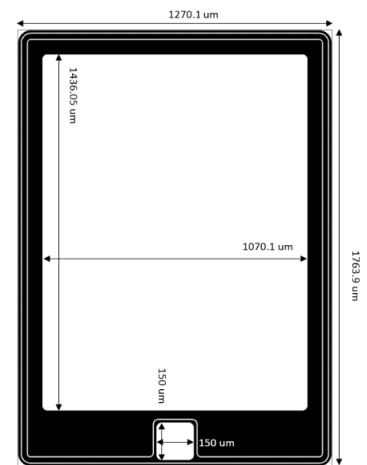
- Advanced Trench Device Design and Processes
- High Reliability Capability
- Sampled CP Probing and Inking


Electrical Characteristics in C/P Test (T_J at 25 °C)

$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	—	—	-40	V	$V_{GS} = 0V, I_D = -250\mu A$
$R_{DS(ON)}$	Static Drain-Source On-Resistance	—	12.5	16.5	m Ω	$V_{GS} = 10V, I_D = -1A^{(2)}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance	—	17.6	21	m Ω	$V_{GS} = 4.5V, I_D = -1A^{(2)}$
$V_{GS(th)}$	Gate Threshold Voltage	-2.5	—	-1.2	V	$V_{DS} = V_{GS}, I_D = -250\mu A$
I_{DSS}	Drain-to-Source Leakage Current	-1	—	—	μA	$V_{DS} = -40V, V_{GS} = 0V$
I_{GSS}	Gate-to-Source Leakage Current	-100	—	100	nA	$V_{DS} = 0V, V_{GS} = \pm 20V$
T_J, T_{STG}	Operating and Storage Temperature	-55°C to 150°C Max.				

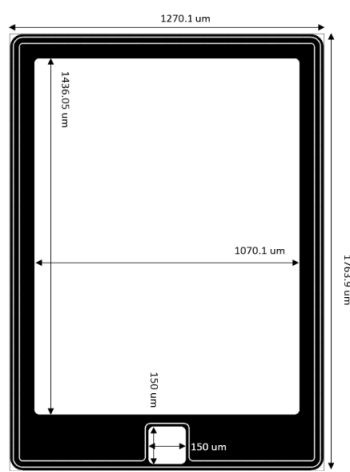
Mechanical Data
Die Drawing


Chip Size ⁽²⁾	1764 μm X 1270 μm
Gate Pad Size	150 μm X 150 μm
Source Pad Size	1436 μm X 1070 μm
Scribe Line Width	60 μm
Wafer Thickness	150 μm
Wafer Diameter	200 mm
Gross Die	11782 EA
Source Metallization	Al-Cu (4 μm typical)
Drain Metallization	Ti-Ni-Ag
Passivation	SiN
Recommended Storage Environment	Store in original container, in dry nitrogen, 6 months at ambient temperature of 23°C \pm 3°C



(1) Pulse Width $t_p = < 1$ mS, Duty Cycle $< 2\%$.

(2) Chip size not include scribe line.

Specific Assembly Information Bill of Material (BOM)		Die Drawing
Package Type	DFN5*6	
Die Attach Method	Soft solder	
Soft Solder Composition	Pb,Sn,Ag	
Gate Wire Bonding	Cu, 2 mil x1	
Source Wire Bonding	Al Ribbon	
Molding Compound Manufacturer	G700HF	
Solder Plating Composition	Pure Tin	

Position			Bonding Diagram Top View
	X (μm)	Y (μm)	
ZERO	0	0	
TOP	1763.9	1270.1	
S1	100	100	
S2	1536.05	1170.1	
G1	1586.05	560.05	
G2	1736.05	710.05	

Electrical Characteristics in F/T Test (T_J at 25 °C)						
I _{DSS}	Drain-to-Source Leakage Current	-1	—	—	μA	V _{DS} = -40V, V _{GS} = 0V
I _{GSSF}	Gate-to-Source Leakage Current	—	—	100	nA	V _{DS} = 0V, V _{GS} = +20V
I _{GSSR}	Gate-to-Source Leakage Current	-100	—	—	nA	V _{DS} = 0V, V _{GS} = -20V
BV _{DSS}	Drain-Source Breakdown Voltage	—	—	-40	V	V _{GS} = 0V, I _D = -250μA
BV _{DSS}	Drain-Source Breakdown Voltage	—	—	-40	V	V _{GS} = 0V, I _D = -1mA
R _{DS(ON)}	Static Drain-Source On-Resistance	—	—	18	mΩ	V _{GS} = -10V, I _D = -11A
R _{DS(ON)}	Static Drain-Source On-Resistance	—	—	25	mΩ	V _{GS} = -4.5V, I _D = -7A
V _{GS(th)}	Gate Threshold Voltage	-2.5	—	-1.2	V	V _{DS} = V _{GS} , I _D = -250μA
V _{SD}	Body Diode Forward Voltage	-1.2	—	—	V	V _{GS} = 0V, I _{SD} = -11A
T _J , T _{STG}	Operating and Storage Temperature	-55	—	150	°C	

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